

☒ Drafts

☐ Pending

☒ Active

- ☒ L1: (66) 708/708.ccls.
- ☒ L2: (755146) transistor\$1
- ☒ L3: (21) 1 and 2
- ☒ L4: (2010342) carry-save or carry adj save or csa or bit-count\$3 or bit adj count\$3 or compress\$3
- ☒ L5: (1217) 708/7???.ccls.
- ☒ L6: (210) 4 and 5
- ☒ L7: (88) 2 and 6
- ☒ L8: (70) 7 not 3
- ☒ L9: (62) 708/629.ccls.
- ☒ L10: (57) 9 not (3 or 8)
- ☒ L11: (13) 2 and 10

☒ Failed

☒ Saved

☒ Favorites

☒ Tagged (5)

☒ UDC

☒ Queue

☒ Trash

 US-PGPUB: USPAT: USOCR: EPO: JPO: DERW
 ☐ Plurals

 Default operator:
☒ Highlight all hit terms initially

☒ BRS form
 ☒ AS&R form
 ☒ Image
 ☒ Text
 ☒ HTML

	U	<input checked="" type="checkbox"/>	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval CI	Inventor	S	C	P	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	Ir
1	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6584485 B1	20030624	8	4 to 2 adder	708/708	708/709; 708/710		Aoki; Naoaki et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	U
2	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6615229 B1	20030902	12	Dual threshold voltage complementary pass-transistor	708/629			Vijayrao; Narsing et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	U
3	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6308195 B1	20011023	38	4-2 compressor circuit and voltage holding circuit for use in	708/708	708/629		Hirase; Yuko et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	U
4	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 5818747 A	19981006	15	Small, fast CMOS 4-2 carry-save adder cell	708/702	708/703; 708/707		Wong; Ming G.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	U
5	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 4556948 A	19851203	14	Multiplier speed improvement by skipping carry save adders	708/625	708/708		Mercy; Brian R.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	U

☒ Hits
 ☒ Details
 ☒ HTML

Ready

NUM

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